Make sure the mapping in Spectcl is modified if changes are made.
(Current/SpecTcl/input-files: *ChipTeleMap.dat* & AdcCsiMap*.dat)*

To monitor spectra:
1. Start SpecTcl
2. Load def-files (Current/SpecTcl/def-files/)
3. Read configuration file (Current/SpecTcl/win-files) -- Xamine
4. Print out the spectra if you find any problem (screen-capture: **gimp** (look at screen-capture in how-to page)

HiRA:
def-files (Current/SpecTcl/def-files/HiRA):
- HiRA_overview.tcl **(RAW)**
- 2d->Pid_clean (loads ef vs de and eb vs de with calibration slopes/offsets, csi veto and gate on eb=ef) **(Calibrated)**

win-files (Current/SpecTcl/win-files/):

RAW Spectra
- summary-> EB_Sum_Raw.win
- summary-> EF_Sum_Raw.win
- summary-> dE_Sum_Raw.win
- summary-> CsI_Sum_Raw.win
- Hit-> EF_EB_Hit.win
- Hit-> DE_EF_Hit.win
- Hit-> DE_EB_Hit.win
- 2D-> 2d_EB_CsI_Raw.win
- 2D-> 2d_EF_CsI_Raw.win

Calibrated Spectra
- with pid_clean   2D->2d_ef_de_mev
- with pid_clean   2D->2d_eb_de_mev

ASIC:
def-files (Current/SpecTcl/def-files/ASIC):
- asic_all_summary.tcl

win-files (Current/SpecTcl/win-files/summary):
- asic_T0-3detconf_sum.win
- asic_T4-5 detconf_sum.win
- asic_CsI_T0_summary.win
- asic_CsI_T1_summary.win
- asic_CsI_T2_summary.win
- asic_CsI_T3_summary.win

S800:
def-files: S800_beam.tcl
win-files:

MCP:
<u>def-files</u>:
<u>win-files</u>:

Calibration files: All are in Current/SpecTcl/vdef-files)
Alpha_all_LOAD_ME.vdef