

TDC channel signal

- 0 MCP 0
- 1 MCP 1
- 2 MCP downscale
- 3 MCP delayed OR
- 4 S800 E1
- 5 XFP
- 6 Pulser bit
- 7 HiRA OR
- 8 S800+HiRA coincidence
- 9 XLM trigger
- 10 HiRA common stop
- 11 RF
- 12 MCP coincidence
- 13 MCP 0 raw (not gated by master)
- 14 MCP 1 raw (not gated by master)
- 15 XFP raw (not gated by master)